



Migration from EN25P40 to EN25F40

Part No. :	EN25F40
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1. Purpose

EN25P40 will EOL and be replaced by EN25F40 which can offer uniform 4Kbytes sector and allow for greater flexibility in applications. This note highlights the difference between those two devices. And it helps customers to migrate into new device.

2. Difference

When customers want to replace EN25P40 with EN25F40, the difference of device ID needs to take care only. Other items in the list below are the additional features of EN25F40 compared with EN25P40.

- **Manufacturer and Device Identification**

EN25F40 :

OP Code	(M7-M0)	(ID15-ID0)	(ID7-ID0)
ABh			12h
90h	1Ch		12h
9Fh	1Ch	3113h	

EN25P40 :

OP Code	(M7-M0)	(ID15-ID0)	(ID7-ID0)
ABh			12h
90h	1Ch		12h
9Fh	1Ch	2013h	



● **High Performance**

EN25F40---100MHz clock rate

EN25P40---75MHz clock rate

● **Block Sector Architecture**

EN25F40 : Small uniform sector Architecture

- 128 sectors of 4KB
- 8 blocks of 64KB
- Any sector or block can be erased individually

EN25P40 : Uniform sector Architecture

- 8 sectors of 64KB
- Any block can be erased individually

● **Erasable**

EN25F40--- Sector, Block or Chip erasable

EN25P40--- Sector or Chip erasable

● **Support Lockable 256 byte OTP security sector**

EN25F40---Yes

EN25P40---No

● **Instruction Set Comparison**

EN25F40 :

Instruction Name	Byte 1 Code	Byte 2	Byte 3	Byte 4	Byte 5	Byte 6	n-Bytes
Sector Erase	20h	A23-A16	A15-A8	A7-A0			
Block Erase	D8h/ 52h	A23-A16	A15-A8	A7-A0			
Chip Erase	C7h/ 60h						
Enter OTP mode	3Ah						

EN25P40 :

Instruction Name	Byte 1 Code	Byte 2	Byte 3	Byte 4	Byte 5	Byte 6	n-Bytes
Sector Erase	D8h	A23-A16	A15-A8	A7-A0			
Bulk Erase	C7h						



Revisions History

Revision No	Description	Date
A	Initial Release.	2007/08/30