Specification Comparison

EN29LV320AB-70TIP VS SST39VF3201

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or modifications due to changes in technical specifications.

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1. Part No.

Eon: EN29LV320AB-70TIP
SST: SST39VF3201

2. Basic Features:

The following features are identical with each other.

- **2.7V – 3.6V Read/Program/Erase Voltage.**
- **As fast as 70 ns read speed**
- **Data# Polling and toggle bits**
- **Erase-suspend / Erase resume**
- **JEDEC standard compatible pin-out and command sets. (Program / Erase)**
- **CMOS I/O compatibility**

3. Identical:

- **Hardware reset pin (RESET#):**
  Hardware method to reset the device to reading array data
  EN29LV320AB-70TIP: Yes
  SST39VF3201: Yes

- **Hardware write protect pin (WP#):**
  Hardware method used for Write Protect function
  EN29LV320AB-70TIP: Yes
  SST39VF3201: Yes

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4. Differences:

- **Word / Byte# configuration:**
  EN29LV320AB-70TIP: Yes (word or byte bus width selection)
  SST39VF3201: No (only word type)

- **Ready/Busy# pin (RY/BY#):**
  Provides a hardware method of detecting program or erase cycle completion
  EN29LV320AB-70TIP: Yes
  SST39VF3201: No

- **Different sector type**
  EN29LV320AB-70TIP: Bottom boot sector, 63 X 64KB sectors
  SST39VF3201: Uniform 4KB sector

- **Manufacture ID:**
  EN29LV320AB-70TIP
  - Manufacturer ID : 1CH / 7Fh
  - Device ID : 22F6h / 22F9h

Description		CE#	OE#	WE#	A20 to A12	A11 to A10	A9[2]	A8	A7	A6	A5 to A2	A1	A0	DQ8 to DQ15	DQ7 to DQ0
Manufacturer ID: Eon		L	L	H	X	X	V_{ID}	H[1]	X	L	X	L	L	X	1Ch
								L							7Fh
Device ID (top boot sector)	Word	L	L	H	X	X	V_{ID}	X	X	L	X	L	H	22h	F6h
	Byte	L	L	H										X	F6h
Device ID (bottom boot sector)	Word	L	L	H	X	X	V_{ID}	X	X	L	X	L	H	22h	F9h
	Byte	L	L	H										X	F9h
Sector Protection Verification		L	L	H	SA	X	V_{ID}	X	X	L	X	H	L	X	01h (Protected)
														X	00h (Unprotected)

L=logic low= V_{IL}, H=Logic High= V_{IH}, V_{ID} =11 ± 0.5V, X=Don't Care (either L or H, but not floating!), SA=Sector Addresses

Note:
1. A8=H is recommended for Manufacturing ID check. If a manufacturing ID is read with A8=L, the chip will output a configuration code 7Fh.
2. A9 = V_{ID} is for HV A9 Autoselect mode only. A9 must be ≤ Vcc (CMOS logic level) for Command Autoselect Mode.

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SST39VF3201:

- Manufacturer ID : BFh

Device ID: 235Bh

	Address	Data
Manufacturer's ID	0000H	BFH
Device ID		
SST39VF1601	0001H	234BH
SST39VF1602	0001H	234AH
SST39VF3201	0001H	235BH
SST39VF3202	0001H	235AH
SST39VF6401	0001H	236BH
SST39VF6402	0001H	236AH

- **Address input for command cycles:**

  EN29LV320AB-70TIP :

  Using address input 555H and 2AAH for command cycles.

  Address bits A_{11-} A_0 are input for 555H and 2AAH, address bits A_{12-} A_{20} are don't cares.

  SST39VF3201:

  Using address input 5555H and 2AAAH for command cycles.

  Address bits A_{15-} A_0 are input for 5555H and 2AAAH, address bits $A_{16\sim}$ $A_{20\ is}$ are don't cares.

  The address input in the command sequence for the SST39VF3201 can also be used for the EN29LV320AB-70TIP without any change.

- **Temporary sector unprotect:**

  This mode allows users to use specific programmer equipment to do programming function.

  EN29LV320AB-70TIP : Yes

  SST39VF3201: No

- **Different VIko value:**

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When VCC is smaller Vlko, any write command is ignore. This is used to protect array data during power on / down procedure.

EN29LV320AB-70TIP: 2.3V ~ 2.5V

SST39VF3201: <1.5V

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